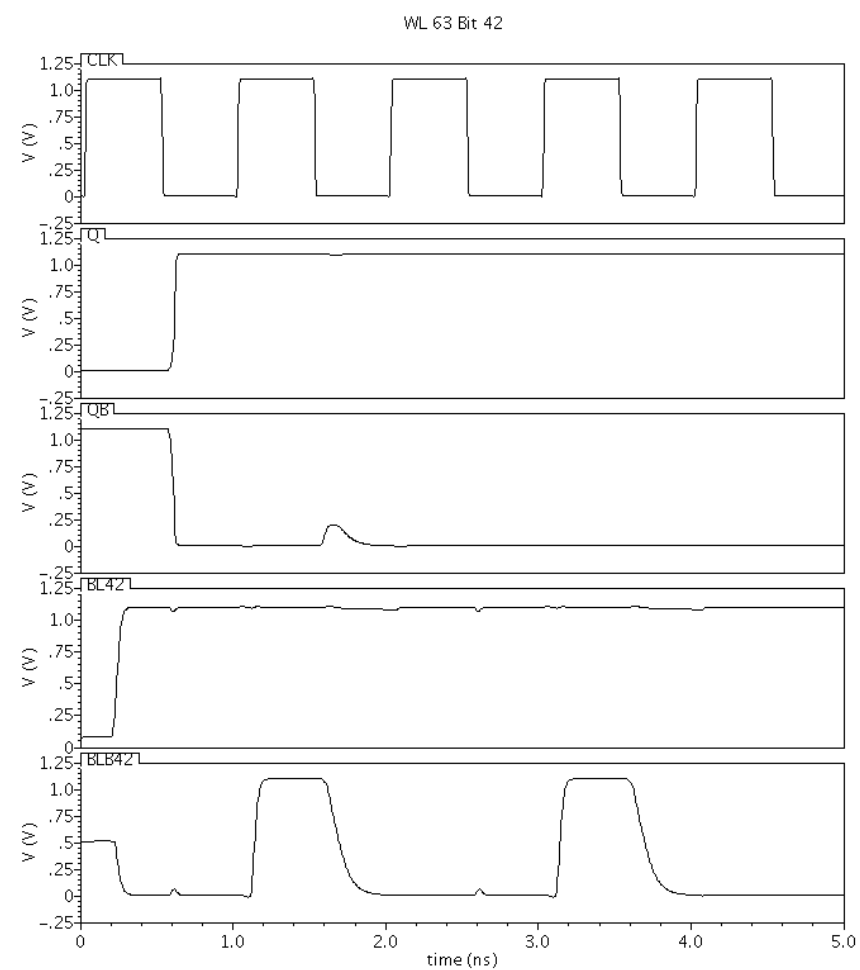
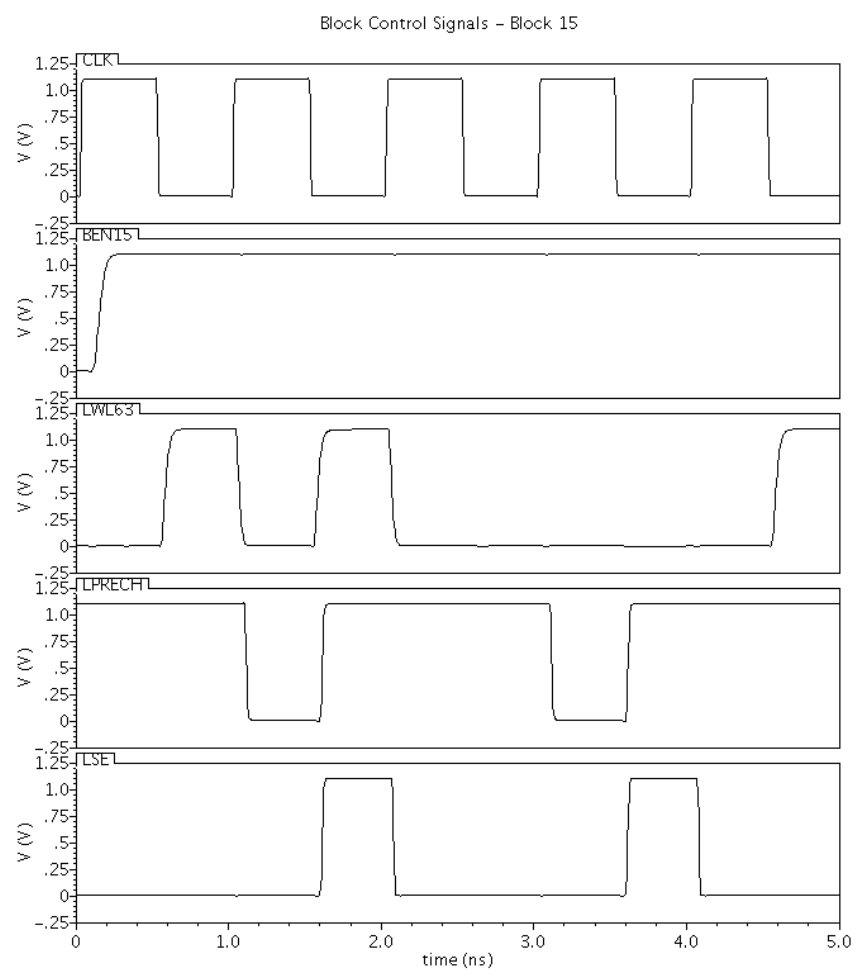
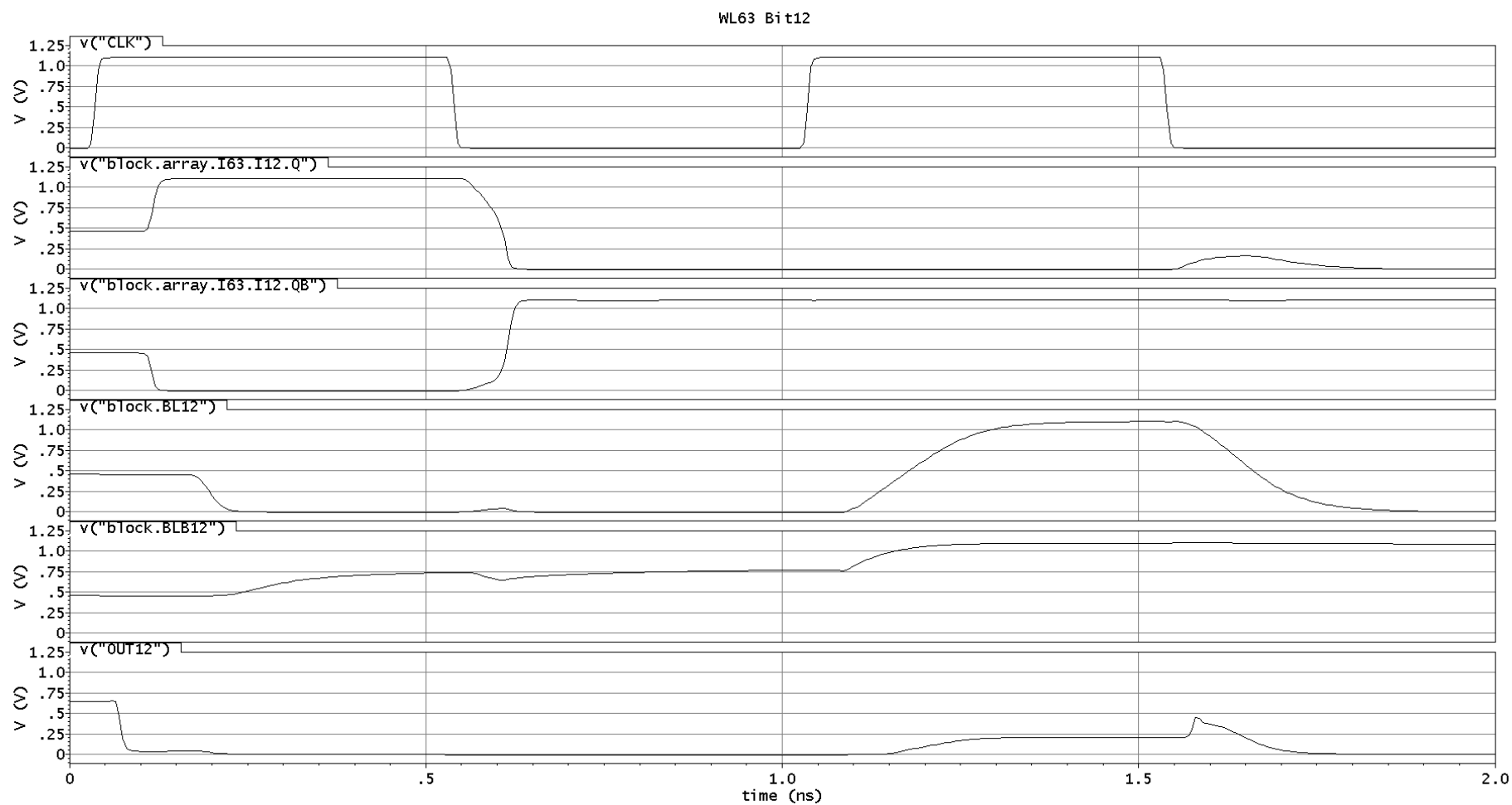


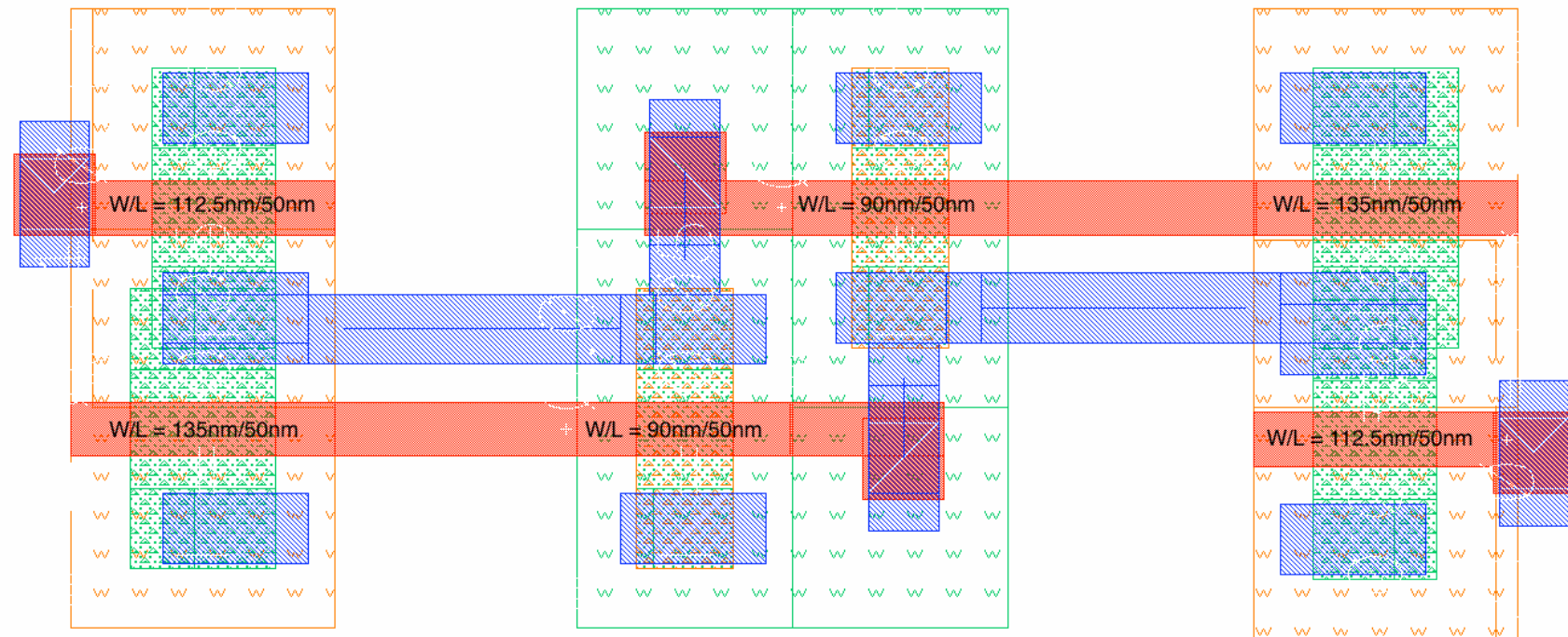
**Figure 1** Global Control Signals, Inputs and Outputs at Specified Bits



**Figure A2** Writing and then Reading a “1” at Block 15 WL63 Bit42



**Figure A4** Output and Key Array Signals when Writing and Reading a “0”



**Figure A5** Proposed 6-T Bitcell Layout